

FIG. 1

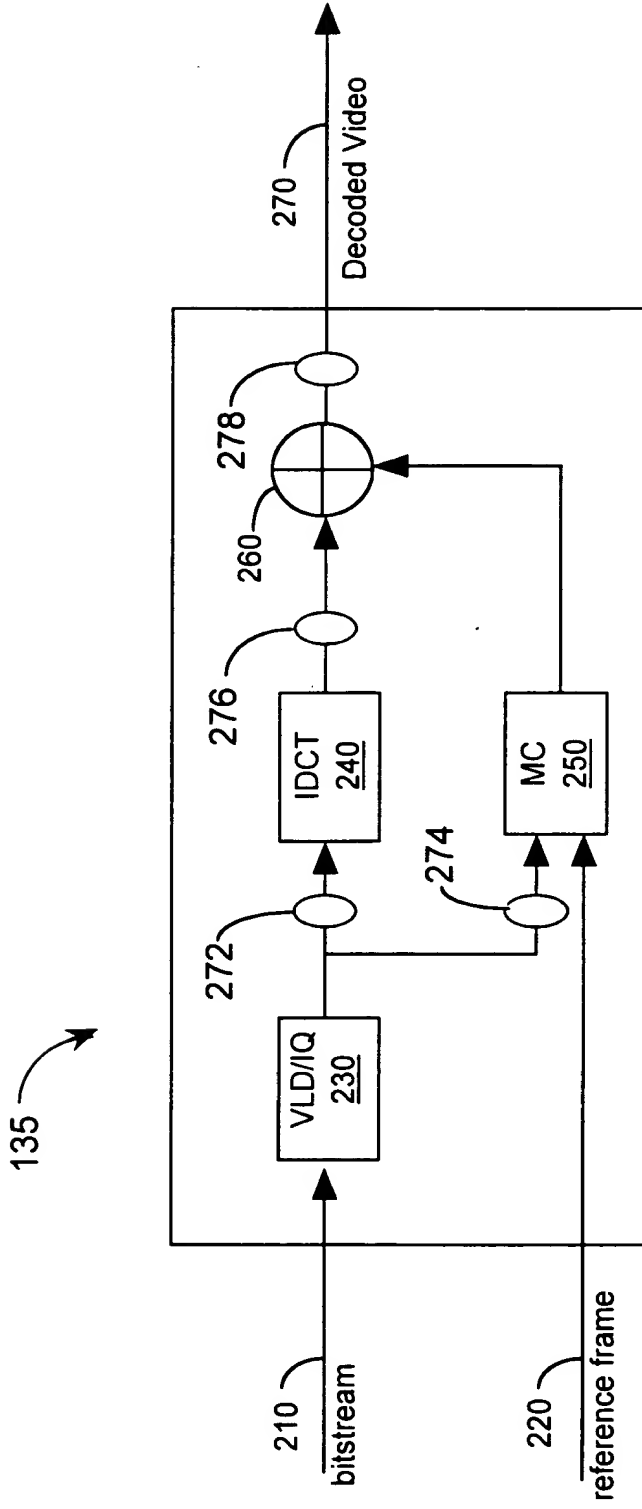
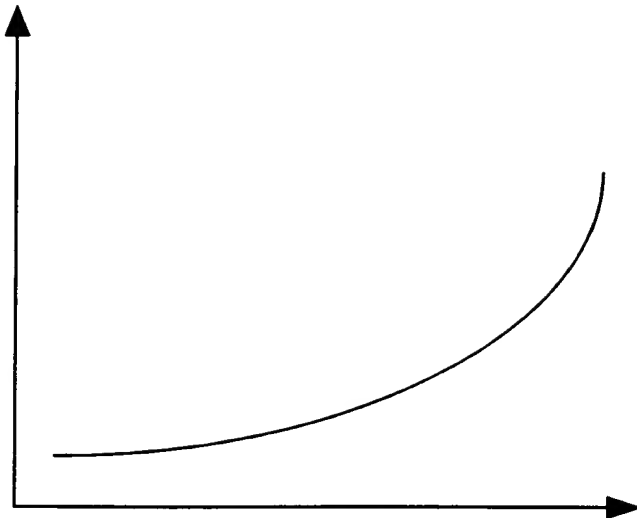


FIG. 2

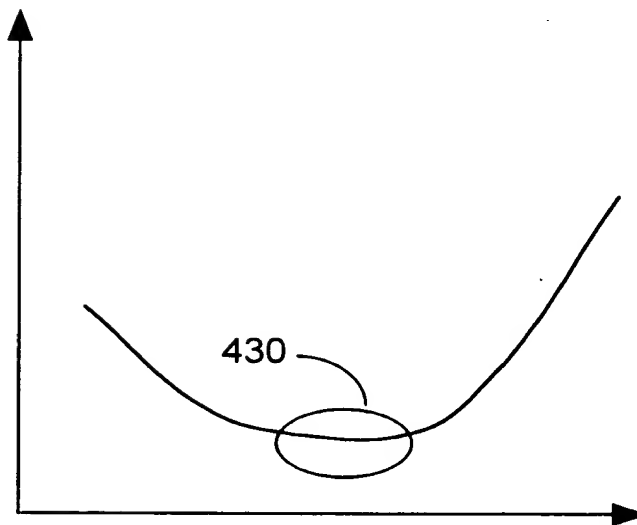
D-cache
Miss-rate



Data Store Buffer Size (increasing)

FIG. 4b

Overall
Cache
Miss-rate



Data Store Buffer Size (increasing)

FIG. 4c

